



# UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
United States Patent and Trademark Office  
Address: COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, Virginia 22313-1450  
[www.uspto.gov](http://www.uspto.gov)

*DRK*

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/960,771	09/21/2001	David N. Pether	00-335 1496.00154	5402
24319	7590	08/04/2004	EXAMINER	
LSI LOGIC CORPORATION 1621 BARBER LANE MS: D-106 LEGAL MILPITAS, CA 95035			NGUYEN, HAU H	
		ART UNIT	PAPER NUMBER	
		2676	<i>jb</i>	
DATE MAILED: 08/04/2004				

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	09/960,771	PETHER, DAVID N.	
	<b>Examiner</b>	<b>Art Unit</b>	
	Hau H Nguyen	2676	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

#### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 03 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

- 1) Responsive to communication(s) filed on 14 May 2004.  
 2a) This action is **FINAL**.                    2b) This action is non-final.  
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

- 4) Claim(s) 1-22 is/are pending in the application.  
 4a) Of the above claim(s) 6 is/are withdrawn from consideration.  
 5) Claim(s) \_\_\_\_\_ is/are allowed.  
 6) Claim(s) 1-5 and 7-22 is/are rejected.  
 7) Claim(s) \_\_\_\_\_ is/are objected to.  
 8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

- 9) The specification is objected to by the Examiner.  
 10) The drawing(s) filed on \_\_\_\_\_ is/are: a) accepted or b) objected to by the Examiner.  
     Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
     Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).  
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
 a) All    b) Some \* c) None of:  
 1. Certified copies of the priority documents have been received.  
 2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)  | 4) <input type="checkbox"/> Interview Summary (PTO-413)                     |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                     | Paper No(s)/Mail Date. _____ .  |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date _____ . | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
|  | 6) <input type="checkbox"/> Other: _____ .                                  |

## DETAILED ACTION

### *Claim Rejections - 35 USC § 102*

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1-5, 7-22 are rejected under 35 U.S.C. 102(b) as being anticipated by Fujimoto (U.S. Patent No. 5,912,710).

Referring to claim 1, 3, 9, 10, 12, 13, 18, 20, and 22, Fujimoto teaches an apparatus 300 reads out graphics data 100G (a first input data stream) and video data 100B (also referred to as "motion picture data") (a second input data stream) recorded on the DVD media 100 and generates image signals for displaying the blended images comprising the graphics data and motion picture data on the television monitor 200 which has a particular display aspect ratio (col. 5, lines 7-15). The image display control apparatus 300, as shown in FIG. 1, is comprised of a DVD ROM drive 101, a MPEG2 decoder 102, a video memory, such as a volatile random access memory (VRAM) 103, a color data controller 104 that includes a RGB color palette 104a and a color space converter 104b, a display controller 155 (and color data controller 104 together forms a first modification circuit), that includes a filtering circuit 105 and a first scalar 106, for changing pixel aspect ratio of the graphics data 100G (to produce a first output data stream), a second scalar 107 (a second data modification circuit) for changing a size of the motion picture data 100B so that it fits in a video window of a given size on the monitor 200 (to produce a

second output data stream), a a-blending circuit 108 (spatially combining) and an NTSC/PAL encoder 109 for providing image signals to the television monitor 200 (col. 5, lines 7-27).

In regard to claims 2, 11, and 19, as shown in Fig. 8, Fujimoto teach ncluded in the display controller 155 is a memory controller 201 for controlling access to the video memory (VRAM) 103, a bit block transfer circuit (Bit Blt) 202 (a block modify and move engine) for executing various logical operations (bitwise logical operation) between bit maps of a transferer and a transferee and scaling up or down the bit maps, a graphics/video mixer 203, and a PCM audio controller 204.

In regard to claims 4, 14, as cited above, Fujimoto teaches the first modification circuit converts input stream of graphics data into an output format between a video picture format and graphics format to be displayed on television monitor 200.

Referring to claim 5, as shown in Fig. 2, Fujimoto illustrates sample displays of a blended image comprising motion picture data and graphics data. In Fig. 2, a screen 20 are displayed the motion picture data displayed in a video window 21 and graphics data 22. The graphics data 22, for example, the contents of an electronic encyclopedia, are displayed as a background image for the video window 21 (spatial combination placing video pictures in front of graphics data) (col. 7, lines 57-67, and col. 8, lines 1-5).

In regard to claims 7 and 16, Fujimoto teach the color space converter 104b converts the RGB color data from the color palette circuit 104a to YCrCb television standard data (col. 7, lines 18-22), and thus including interleaving RGB color components.

Referring to claims 8 and 17, as shown in Fig. 10, Fujimoto teaches RBG/YCrCb color space converter 104b is comprised of three color tables of 10R, 10G and 10B for respectively

corresponding to the red (R), green (G) and blue (B) colors. Each of the color tables 10R, 10G and 10B is comprised of 256 entries and an address decoder for selecting one entry among the 256 entries by decoding the pixel data of 8 bits. Each of the entries stores the color data of 8 bits (col. 12, lines 13-20). Thus, it is implied that a separation of color components is performed in the space color converter 104b.

In regard to claim 15, as shown in Fig. 3, Fujimoto teaches the motion picture image of the sample object 24 (e.g., the dolphin), along with the graphics data images including the title 23, attributes 25, image data 26 and operation buttons 27 are displayed in full screen mode (col. 8, lines 6-12). As depicted in Fig. 3, graphics information, for example operation buttons is placed in front of video data (the dolphin 24).

Referring to claim 21, as shown in Figs. 1, 8, and 9, Fujimoto teaches video data (a first data format) are generated from the MPEG2 decoder 102 (a first expander circuit), graphics data (a second data format) are generated from the display controller 155 (a second expander circuit). In the  $\alpha$ -blending circuit 108 in FIG. 9, the graphics data and the DVD video data are combined as a function of the  $\alpha$  value (a third data) of the pixel position corresponding to the displaying target line (spatial combination). This blending is performed at a timing (a control signal) in accordance with the synchronization signals in the DVD video data detected by the timing control circuit 204 in FIG. 9 (a third expander circuit). As also shown in Fig. 9, FIFO 312a is used to buffer graphics data, and FIFO 312b is used to buffer a third ( $\alpha$ ) data. Although not stated, it is implied that the video data (first data) is buffered in a memory in order to process  $\alpha$ -blending (spatial combination).

***Conclusion***

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hau H. Nguyen whose telephone number is: 703-305-4104. The examiner can normally be reached on MON-FRI from 8:30-5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew Bella can be reached on 703-308-6829.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

Washington, D. C. 20231

or faxed to:

(703) 872-9314 (for Technology Center 2600 only)

Hand-delivered response should be brought to Crystal Park II, 2121 Crystal Drive, Arlington, VA, Sixth floor (Receptionist).

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Technology Center 2600 Customer Service Office whose telephone number is (703) 306-0377.



MATTHEW C. BELLA  
SUPERVISORY PATENT EXAMINER  
TECHNOLOGY CENTER 2600

H. Nguyen

07/22/2004